S/N 09/945,512 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes

Examiner: Richard A. Booth

Serial No.:

09/945,512

Group Art Unit: 2812

Filed:

August 30, 2001

Docket: 1303.027US1

Title:

IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL

BARRIER INTERPOLY INSULATORS

TERMINAL DISCLAIMER

MS AF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

I, Timothy B. Clise, am the attorney of record for the above identified patent application as evidenced by the Power of Attorney filed in the present application on August 30, 2001. I am making this petition on behalf of Micron Technology, Inc., the assignee of the present invention. As the attorney of record, I am empowered to act on behalf of the assignee and, in accordance with 37 C.F.R. § 1.321(b)(iv), to sign this terminal disclaimer.

Certificate Under 37 C.F.R. § 3.73(b)

Your petitioner, Micron Technology, Inc., certifies that it is the owner of the entire right, title and interest in and to the above-identified patent application (Serial No. 09/945,512) and to U.S. Serial Nos. 10/929,916; 10/931,704; 10/929,986; 10/931,711; and 10/931,540. Your petitioner owns the entire right, title, and interest of these applications by nature of the assignments executed and filed for all of these applications. The assignment for U.S. Serial No. 09/945,512 was recorded on 8/30/2001 at Reel/Frame 012154/0085 with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 10/929,916 was recorded on 12/20/2001 at Reel/Frame 012413/0348 with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 10/931,704 was recorded on 8/30/2001 at Reel/Frame 012154/0149 with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 10/929,986 was recorded on 2/20/2002 at Reel/Frame 012633/0093 with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 10/931,711 was recorded on 8/30/2001 at Reel/Frame 012155/0529 with the United States Patent and Trademark Office. The assignment for U.S. Serial No. 10/931,540 was recorded on 8/30/2001 at Reel/Frame 012142/0662 with the United States Patent and Trademark Office.

TERMINAL DISCLAIMER

Serial Number: 09/945,512

Filing Date: August 30, 2001

Title: IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Page 2

Dkt: 1303.027US1

The undersigned representative of the assignee has reviewed the evidentiary documents of title and certifies that to the best of assignee's knowledge and belief, title is in the assignee, Micron Technology, Inc., seeking to take the action set forth in this disclaimer.

Terminal Disclaimer

Your petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the above-identified patent application, which would extend beyond the expiration date of the full statutory term, as presently shortened by any terminal disclaimers, of any patent which may issue from U.S. Serial Nos. 10/929,916; 10/931,704; 10/929,986; 10/931,711; and 10/931,540. Petitioner hereby agrees that any patent so granted on the above-identified application shall be enforceable only for and during such period that the legal title to any patent which may issue from U.S. Serial Nos. 10/929,916; 10/931,704; 10/929,986; 10/931,711; and 10/931,540 and the legal title of the above-identified application and any patent granted thereon remain common, this agreement to run with any patent granted on the above-identified application and to be binding upon the grantee, its successors and assigns.

Limitations on the Disclaimer

Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application prior to the expiration date of the full statutory term as presently shortened by any terminal disclaimer of any patent which may granted on U.S. Serial Nos. 10/929,916; 10/931,704; 10/929,986; 10/931,711; and 10/931,540 in the event that any later expire for failure to pay a maintenance fee, are held unenforceable, are found invalid, are statutorily disclaimed in whole or terminally disclaimed under 37 C.F.R. § 1.321(a), are reissued, have all claims canceled by a reexamination certificate, or are otherwise terminated prior to the expiration date of its respective statutory term as presently shortened by any terminal disclaimer, except for the separation of legal title as stated hereinabove. Your petitioner does not disclaim any terminal part of any patent granted on the above-identified application if no patent issues from U.S. Serial Nos. 10/929,916; 10/931,704; 10/929,986; 10/931,711; and 10/931,540, respectively.

Serial Number: 09/945,512

Dkt: 1303.027US1 Filing Date: August 30, 2001 IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

Fee Status

The Commissioner of Patents and Trademarks is hereby authorized to charge Deposit Account 19-0743 in the amount of \$130.00, which is required under 37 C.F.R. § 1.20(d) to file a statutory disclaimer. The Commissioner of Patents and Trademarks is hereby authorized to charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. Box 2938 Minneapolis, MN 55402

Page 3

(612) 349-9587

Timothy B. Clise

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this **3** day of **January**, 2006.